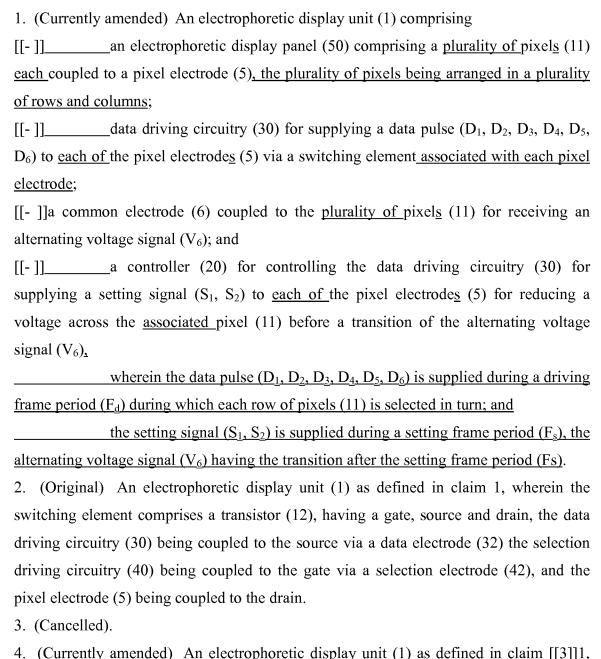
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## **AMENDMENTS TO THE CLAIMS**

Please amend the claims of this application as follows:



wherein the data pulse (D<sub>1</sub>, D<sub>2</sub>, D<sub>3</sub>, D<sub>4</sub>, D<sub>5</sub>, D<sub>6</sub>) is supplied during more than one

consecutive driving frame period (F<sub>d</sub>).

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- 5. (Currently amended) An electrophoretic display unit (1) as defined in claim [[3]] $\underline{1}$ , wherein the setting frame period ( $F_s$ ) is shorter than the driving frame period ( $F_d$ ).
- 6.. (Currently amended) An electrophoretic display unit (1) as defined in claim 1, wherein the alternating voltage signal ( $V_6$ ) and the setting signal ( $S_1$ ,  $S_2$ ) have equal polarities during [[a]]the setting frame period ( $F_s$ ).
- 7.. (Currently amended) An electrophoretic display unit (1) as defined in claim 1, wherein [[an]]the amplitude of the alternating voltage signal ( $V_6$ ) and [[an]]the amplitude of the setting signal ( $S_1$ ,  $S_2$ ) are substantially equal to each other during [[a]]the setting frame period (Fs).
- 8.. (Currently amended) An electrophoretic display unit (1) as defined in claim 1, wherein the controller (20) is adapted to control the data driving circuitry (30) to provide any one or more of:
- shaking data pulses;
- one or more reset data pulses; and
- one or more driving data pulses;
- to [[the]]each pixel (11).
- 9.. (Original) A display device comprising an electrophoretic display unit (1) as defined in claim 1; and a storage medium for storing information to be displayed.
- 10. A method of driving an electrophoretic display unit (1) comprising an electrophoretic display panel (50), which comprises a <u>plurality of pixels</u> (11) <u>each</u> coupled to a pixel electrode (5), <u>the plurality of pixels being arranged in a plurality of rows and columns, which method comprises the steps of</u>
- [[-]] during a driving frame period ( $F_d$ ) during which each row of pixels (11) is selected in turn, supplying a data pulse ( $D_1$ ,  $D_2$ ,  $D_3$ ,  $D_4$ ,  $D_5$ ,  $D_6$ ) to each of the pixel electrodes (5);
- [[-]] supplying an alternating voltage signal  $(V_6)$  to a common electrode (6) coupled to the plurality of pixels (11) via a switching element; and

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[[-]] controlling [[the]]data driving circuitry (30) for supplying, during a setting frame period  $(F_s)$ , a setting signal  $(S_1, S_2)$  to each of the pixel electrodes (5) for reducing a voltage across the associated pixel (11) before a transition of the alternating voltage signal  $(V_6)$  occurring after the setting frame period  $(F_s)$ .

11. (Cancelled).